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APPLICATION NO.	FI	ILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/780,533	33 02/17/2004		Beomsup Kim	BEKAP002C1	7487
21912	7590	12/16/2004	EXAMINER		
VAN PELT & YI LLP 10050 N. FOOTHILL BLVD #200				KINKEAD, ARNOLD M	
CUPERTIN				ART UNIT	PAPER NUMBER
				2817	

DATE MAILED: 12/16/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

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 Claim(s) 7-13 is/are pending in the application. 4a) Of the above claim(s) is/are withdrawn from consideration. Claim(s) 13 is/are allowed. Claim(s) 7-9 is/are rejected. Claim(s) 10-12 is/are objected to. Claim(s) are subject to restriction and/or election requirement. 						
accepted or b) objecte (s) be held in abeyance. See equired if the drawing(s) is ob . Note the attached Office	e 37 CFR 1.85(a). jected to. See 37 CFR 1.121(d).					
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DETAILED ACTION

Claim Objections

1. Claims 7 and 12 objected to because of the following informalities: In claim 7, last line "stages" should read—stage—

2. In claim 12, the dependency should be from – claim 11–. Appropriate correction is required.

Claim Rejections - 35 USC § 102

 The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 2. Claims 7,8, and 9 are rejected under 35 U.S.C. 102(b) as being anticipated by 10-0278551 Korea of record.

The reference shows a multiple stage LC oscillator(see figures 8,9, and 10) comprising a plurality of LC oscillator stages that are coupled to preceding and succeeding stages(see figure 10). Each stage having more than two inputs and two outputs going to a plurality of adjacent stages. Note that one stage in figure 9 has more than two outputs for coupling to adjacent stages. The first stage in figure 10, leftmost stage receives a feedback from the second stage output(the middle stage).

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Double Patenting

3. The nonstatutory double patenting rejection is based on a judicially created doctrine grounded in public policy (a policy reflected in the statute) so as to prevent the unjustified or improper timewise extension of the "right to exclude" granted by a patent and to prevent possible harassment by multiple assignees. See *In re Goodman*, 11 F.3d 1046, 29 USPQ2d 2010 (Fed. Cir. 1993); *In re Longi*, 759 F.2d 887, 225 USPQ 645 (Fed. Cir. 1985); *In re Van Ornum*, 686 F.2d 937, 214 USPQ 761 (CCPA 1982); *In re Vogel*, 422 F.2d 438, 164 USPQ 619 (CCPA 1970); and, *In re Thorington*, 418 F.2d 528, 163 USPQ 644 (CCPA 1969).

A timely filed terminal disclaimer in compliance with 37 CFR 1.321(c) may be used to overcome an actual or provisional rejection based on a nonstatutory double patenting ground provided the conflicting application or patent is shown to be commonly owned with this application. See 37 CFR 1.130(b).

Effective January 1, 1994, a registered attorney or agent of record may sign a terminal disclaimer. A terminal disclaimer signed by the assignee must fully comply with 37 CFR 3.73(b).

4. Claim 7 is rejected under the judicially created doctrine of obviousness-type double patenting as being unpatentable over claim 1 of U.S. Patent No. 6,724,267. Although the conflicting claims are not identical, they are not patentably distinct from each other because The claim in the current application is merely a broader presentation now being claimed for the multiple stage ring oscillator with first, second and third stages and corresponding input/output connections.

Allowable Subject Matter

- 5. Claim 10-12 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.
- 6. Claims 13 is allowed.

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Arnold M Kinkead whose telephone number is 571-272-1763. The examiner can normally be reached on Mon-Fri, 8:30 am -5 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Pascal can be reached on 571-272-1769. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Arnold M Kinkead Primary Examiner Art Unit 2817

Arnold Kinkead Dec. 13, 2004